Instructions for Written Test / Interview for the post of Assistant Professor (Computer Science and Engineering / Electronics and Communication Engineering and Electrical Engineering) in response to advertisement No. 229/2023 dated 10.12.2023

- 1. This has reference to your application for the post of Assistant Professor (Computer Science & Engineering / Electronics & Communication Engineering / Electrical Engineering) in response to the advertisement referred above. The selection process will be carried out in **two stages**.
- 2. During the **first stage** on **day one**, the candidate would be required to appear in the written test comprising of 100 MCQs as per the syllabus prescribed by the institute. The top 15 qualified candidates (scoring 50% or more marks) will move to the **second stage** comprising of presentation and interview on **day two**. In case, if candidates less than 15 qualify the written test, then only those candidates who scores 50% or more marks shall be called for presentation/interview.
- 3. The written test will consist of 100 objective type questions (Multiple Choice Questions) carrying a maximum of 100 marks of 2 hours duration.
- 4. The question paper will have items (questions) in ENGLISH only.
- 5. There will be *four* options for the answers to each question. The candidate will have to select only one out of four options as his/her correct answer. More than one answer or answer rubbed or covered with white fluid or tampered will not be evaluated / considered.
- 6. For each wrong answer, one fourth (0.25) of the marks assigned to that question will be deducted as penalty. If it is left blank i.e. no answer is given by the candidate, there will be no penalty for that question.
- 7. Candidates are permitted to use simple scientific calculators only for answering objective type questions.
- 8. During the second stage on day two, before interview, the candidate would be required to give a presentation (should not be more than 10 minutes and should comprise of 5-8 slides) with respect to his/her teaching ability and research aptitude and his/her prospective role in the department (if selected). During the interview and presentation, the candidate would be assessed on parameters like communication skills, presentation skills, organization of the contents and domain knowledge.
- 9. The presentation will be followed by the interview before the Selection Committee on day two in the Board Room of the institute. Candidates are required to report at 09:00 a.m. sharp.
- 10. All the candidates are required to attend the above sessions (stage one and stage two) in physical mode.

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- 11. For the purpose of document verification, the candidate is required to bring all the original certificates and testimonials regarding academic qualification, category (reservation-SC / ST / OBC / EWS / PwD), work experience, and research experience. The candidate shall have to bring the evidence of indexing / citations regarding the details mentioned in his/her online application along with a valid photo-id document (Aadhaar Card / PAN Card / Passport / Voter ID Card / Driving License). In addition, set of self-attested photocopy of the testimonials, certificates, publications, for submission at the time of presentation.
- 12. Appearing in the written test / interview is provisional and subject to fulfillment of all the requirements of eligibility, experience and other terms and conditions as given in the advertisement.
- 13. No T.A/D.A. shall be paid to the candidate for attending the written test / interview.
- 14. The candidates called for written test / interview may be required to stay till late evening. The working lunch and tea will be available in the institute mess / canteen on payment basis. The candidates are also advised to arrange accommodation on their own.
- 15. Canvassing in any form on behalf of any candidate will disqualify such candidate.

SCHEDULE OF WRITTEN TEST / INTERVIEW FOR THE POST OF ASSISTANT PROFESSOR

Sr. No.	Name of the Post	Date of Written Test	Date of Interview
1	Assistant Professor (Electronics & Communication Engineering)	10th June, 2024 (10:00 AM-12:00 Noon)	11th June, 2024 (2:00 PM-onwards)
2	Assistant Professor (Electrical Engineering)	11th June, 2024 (10:00 AM-12:00 Noon)	12th June, 2024 (2:00 PM-onwards)
3	Assistant Professor (Computer Science & Engineering)	12th June, 2024 (10:00 AM-12:00 Noon)	13th June, 2024 (10:00 AM onwards)

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